

Attorney's Docket No.: 10559-395001  
Client's Ref. No.: P10260 - ADI APD1811-1-US

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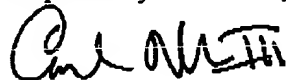
Applicant : Juan G. Revilla et al.      Art Unit : 2111  
Serial No. : 09/823,095      Examiner : Khanh Dang  
Filed : March 29, 2001      Assignee: Intel Corporation and Analog Devices, Inc.

Title : EARLY EXCEPTION DETECTION

**Mail Stop Issue Fee**  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Attached to this facsimile communication cover sheet are an Amendment Under 37 CFR 1.312 and a Transmittal Letter, faxed this 24<sup>th</sup> day of March, 2005, to the United States Patent and Trademark Office.

Respectfully submitted,



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Reg. No. 42,773 for

Date: March 24, 2005

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## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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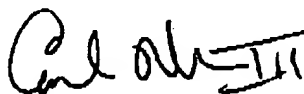
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TRANSMITTAL LETTER

Correspondence relating to this application is enclosed. The required fees are computed below. Please apply any charges not covered, or any credits, to Deposit Account No. 06-1050.

Total Claims	12	-	12	=	0	\$0
Independent	2	-	2	=	0	\$0
TOTAL FEE DUE						\$0

Respectfully submitted,

Date: 3-24-2005

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AMENDMENT UNDER 37 CFR §1.312

Amendments to the claims begin on page 2 of this paper.

Remarks/Arguments begin on page 6 of this paper.

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